## AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application.

**Listing of Claims:**

Please amend the claims as follows:

Claim 1 (Currently Amended): An in-plane switching mode liquid crystal display device comprising:

first and second substrates having an array region and a sealant region along a periphery of the array region, wherein the array region includes a plurality of pixel regions defined by a plurality of gate lines and data lines on the second substrate and the sealant region includes a plurality of gate pads and data pads at an end of the gate and data lines;

a sealant in the sealant region attaching the first and second substrates, wherein the sealant is located over the gate and data pads;

a single metallic black matrix formed in the sealant region that extends into the array region of the first substrate;

a color filter on the metallic black matrix extending into the array region from the sealant region;

a common electrode and a pixel electrode on the second substrate in the array region;

an organic layer on the color filter in the array region, the organic layer covering at least a portion of the single metallic black matrix to shield an electric field in the array region, wherein the organic layer is formed in the array region and in the sealant region; and

a liquid crystal layer between the first and second substrates.

Claim 2 (Original): The device of claim 1, wherein the metallic black matrix is one of Cr and $CrO_x$.

Claim 3 (Original): The device of claim 1, wherein the organic layer is formed in the array region.

Claim 4 (Original): The device of claim 3, wherein the organic layer is in direct contact with the metallic black matrix.

Claim 5 (Cancelled):

Claim 6 (Currently Amended): The device of claim [[5]] 1, wherein the organic layer is in direct contact with the sealant.

Claim 7 (Cancelled).

Claim 8 (Currently Amended): A method for fabricating an in-plane switching mode liquid crystal display device, comprising:

providing first and second substrates having a sealant region and an array region, wherein the array region includes a plurality of pixels defined by a plurality of gate lines and data lines on the second substrate and the sealant region includes a plurality of gate pads and data pads at an end of the gate and data lines;

forming a single metallic black matrix in the sealant region that extends into the array region of the first substrate;

forming a color filter on the metallic black matrix extending into the array region from the sealant region;

forming a pixel electrode and a common electrode on the second substrate in the array region;

forming an organic layer on the color filter in the array region, the organic layer covering at least a portion of the single metallic black matrix to shield an electric field in the array region, wherein the organic layer is formed in the sealant region and the array region;

forming a sealant over the gate and data pads of the sealant region; and

attaching the first and second substrates by the sealant.

Claim 9 (Original): The method of claim 8, wherein the metallic black matrix is one of Cr and $CrO_x$.

Claim 10 (Original): The method of claim 8, wherein the organic layer is formed in the array region.

Claim 11 (Cancelled)

Claim 12 (Cancelled)

Claim 13 (Original): The method of claim 8, further comprising:

forming a liquid crystal layer between the first and second substrates.

Claim 14 (Previously Presented): The device of claim 1, wherein the black matrix extends over at least one thin film transistor in the array region.

Claim 15 (Previously Presented): The device of claim 8, wherein the black matrix extends over at least one thin film transistor in the array region.